



JPW

1248-0673P

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: YUTAKA TAKAFUJI et al
Serial No.: 10/668,186 Group:
Filed: SEPTEMBER 24, 2003 Examiner:
For: SINGLE-CRYSTAL SILICON SUBSTRATE, SOI SUBSTRATE, SEMICONDUCTOR
DEVICE, DISPLAY DEVICE, AND MANUFACTURING METHOD OF
SEMICONDUCTOR DEVICE

REQUEST FOR A CORRECTED OFFICIAL FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

JUL - 1 2004

Sir:

Attached hereto is the Official Filing Receipt in connection with
the above-identified application.

THE FOLLOWING CORRECTION(S) IS/ARE RESPECTFULLY REQUESTED:

FOREIGN APPLICATION PRIORITY DATA:

CHANGE FROM: "JAPAN 2002-280076; JAPAN 2003-299577"
TO: --JAPAN 2002-280078; JAPAN 2002-299577--

It is respectfully requested that the United States Patent and
Trademark Office forward a new Filing Receipt showing the correction(s)
to the undersigned attorney as listed on the enclosed photocopy of our
Declaration and Power of Attorney Document.

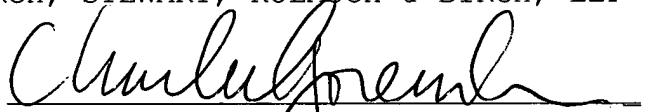
The correction(s) is/are due to an error by the United States
Patent and Trademark Office, therefore no fee is due.

10/668,186

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

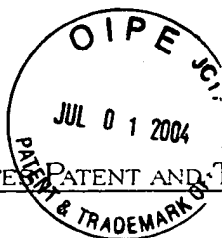
BIRCH, STEWART, KOLASCH & BIRCH, LLP

By: 

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Attachment
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UNITED STATES PATENT AND TRADEMARK OFFICE

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| APPL NO. | FILING OR 371 (c) DATE | ART UNIT | FIL FEE REC'D | ATTY DOCKET NO | DRAWINGS | TOT CLMS | IND CLMS |
|------------|---------------------------|----------|---------------|----------------|----------|----------|----------|
| 10/668,186 | 09/24/2003 | 2812 | 2322 | 1248-0673P | 17 | 56 | 14 |

CONFIRMATION NO. 4125

2292
BIRCH STEWART KOLASCH & BIRCH
PO BOX 747
FALLS CHURCH, VA 22040-0747

FILING RECEIPT



OC000000011549658

Date Mailed: 12/22/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s)

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Takashi Itoga, Nara-shi, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-280076 09/25/2002
JAPAN 2003-299577 10/11/2002
JAPAN 2003-067109 03/12/2003

If Required, Foreign Filing License Granted: 12/20/2003

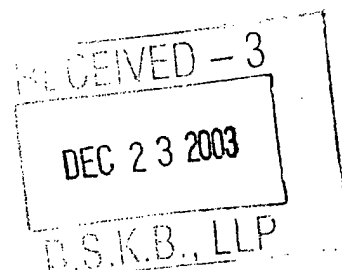
Projected Publication Date: 04/01/2004

Non-Publication Request: No

Early Publication Request: No

Title

Single-crystal silicon substrate, SOI substrate, semiconductor device, display device, and manufacturing method of semiconductor device



Preliminary Class

438

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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